



Layout PCB for surface mount socket

DUTs
Sheet2.sch
AD0-7 ADDRESS
VCC
GND

DTR OFF HIGH = RUN
ON LOW = LOAD
EA HIGH = INTERNAL
LOW = EXTERNAL

Revision History:

Ltr:	Description:	Date:	Approved by:
A	New Design	9/26/02	CR

Design Engineer _____ Date _____
Project Manager _____ Date _____



Title **RTC Reference Design Micro. Inf.** Dallas Semiconductor
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Size: Tabloid Number1 Revision: A
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File: F:\Boards\Applications\Rtc_ref_design.ddb - Documents\Sheet1.Sch