

Reg00:	PRATE 10 (20MHz - 40MHz)	SRATE 10 (200 - 400Mbps)	PAREN 0 (Disable)	PWIDTH 000 (10 bits)	Write	Read	Write Both 9258&9257 <input checked="" type="checkbox"/>				
Reg01:	SPREAD 000 (Off)	SAWDV 11111			Write	Read					
Reg02:	STODIV 1010 (1024)	STOCNT 0000 (0)			Write	Read	<input checked="" type="checkbox"/>				
Reg03:	ETODIV 1010 (1024)	ETOCNT 1001 (9)			Write	Read	<input checked="" type="checkbox"/>				
Reg04:	VEDGE Falling	HEDGE Falling	CKEDGE Rising	PD Power U	SEREN Enable	BYPPPLL Active	TRN2X Normal	PRBSEN Enable	Write	Read	
Reg05:	DEVICEID 1111 101	Write	Read	Reg06:	ENDID 1111 111	Write	Read	Reg07:	MAX9258 ID 1111 100	Write	Read
Reg08:	INTMODE UART	INTEN Enable	FAST 0 - 4Mbps	CTO 100 (64 bits)	BITRATE 00 (95 - 400Kbps)	Write	Read	<input checked="" type="checkbox"/>			
Reg09:	PRBSLEN 1111 (Continuous)	GPIO9DIR Input	GPIO8DIR Input	GPIO9 0	GPIO8 0	Write	Read				
Reg10:	GPIO7DIR Input	GPIO6DIR Input	GPIO5DIR Input	GPIO4DIR Input	GPIO3DIR Input	GPIO2DIR Input	GPIO1DIR Input	GPIO0DIR Output	Write	Read	
Reg11:	GPIO7 0	GPIO6 0	GPIO5 0	GPIO4 0	GPIO3 0	GPIO2 0	GPIO1 0	GPIO0 1	Write	Read	
Reg12:	PREEMP 0000	CPCUR 0000	Write	Read	Reg13:	I2CFILT 00 (According to bit rate)	Write	Read			
Reg14:	LOCKED	Read	Reg15 (Bit length):				Decimal	RITIFN	Read		

